

Fig. 4
OVERALL FLOW

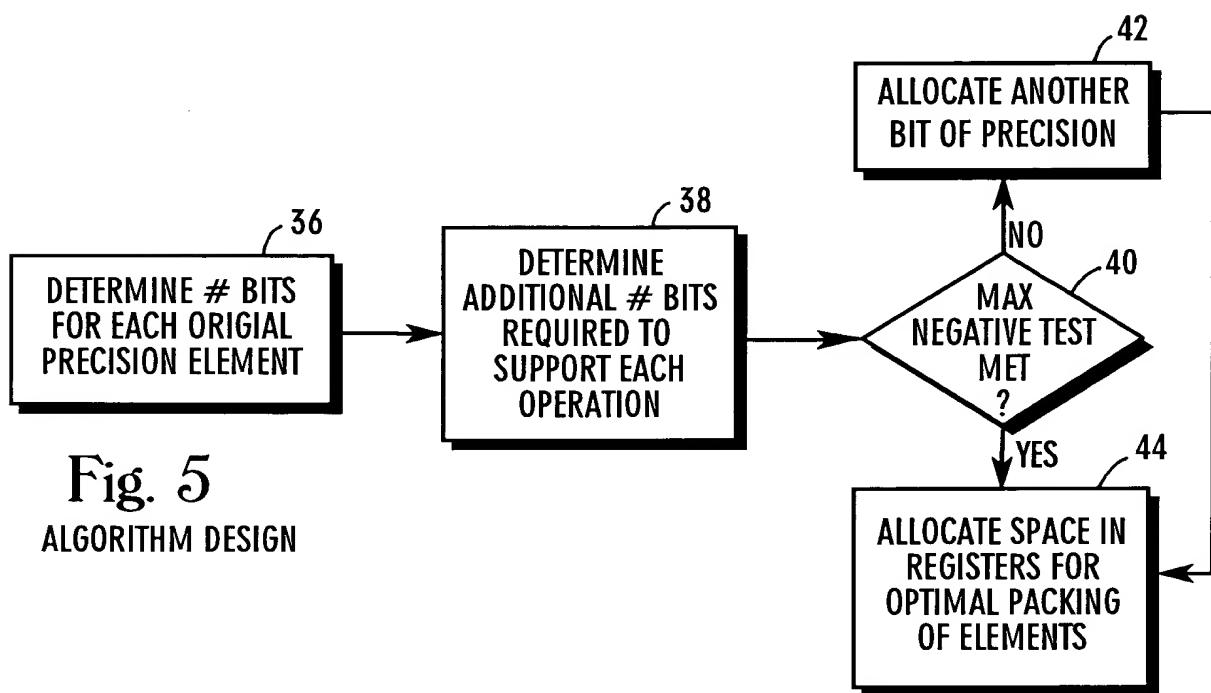


Fig. 5
ALGORITHM DESIGN

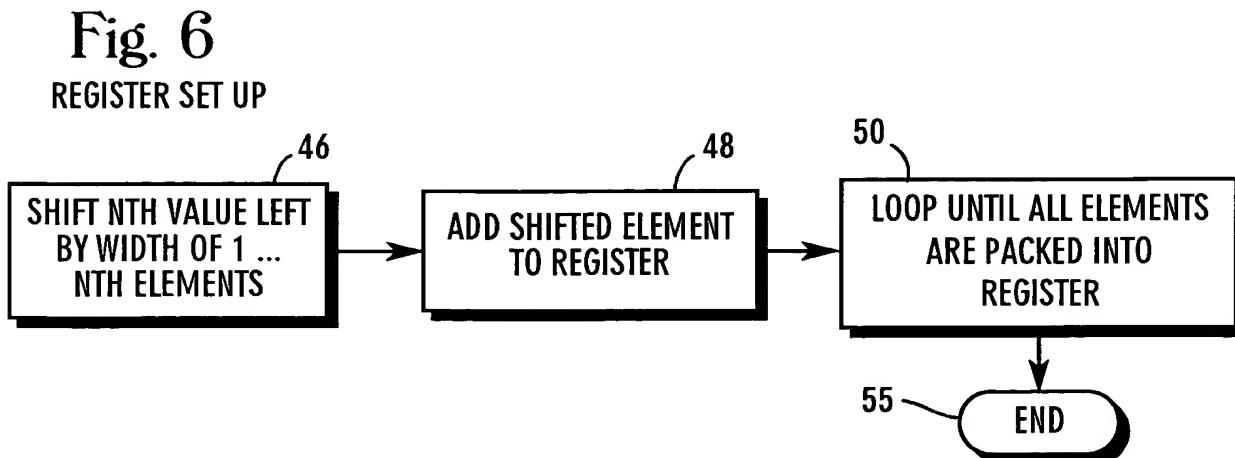


Fig. 6
REGISTER SET UP

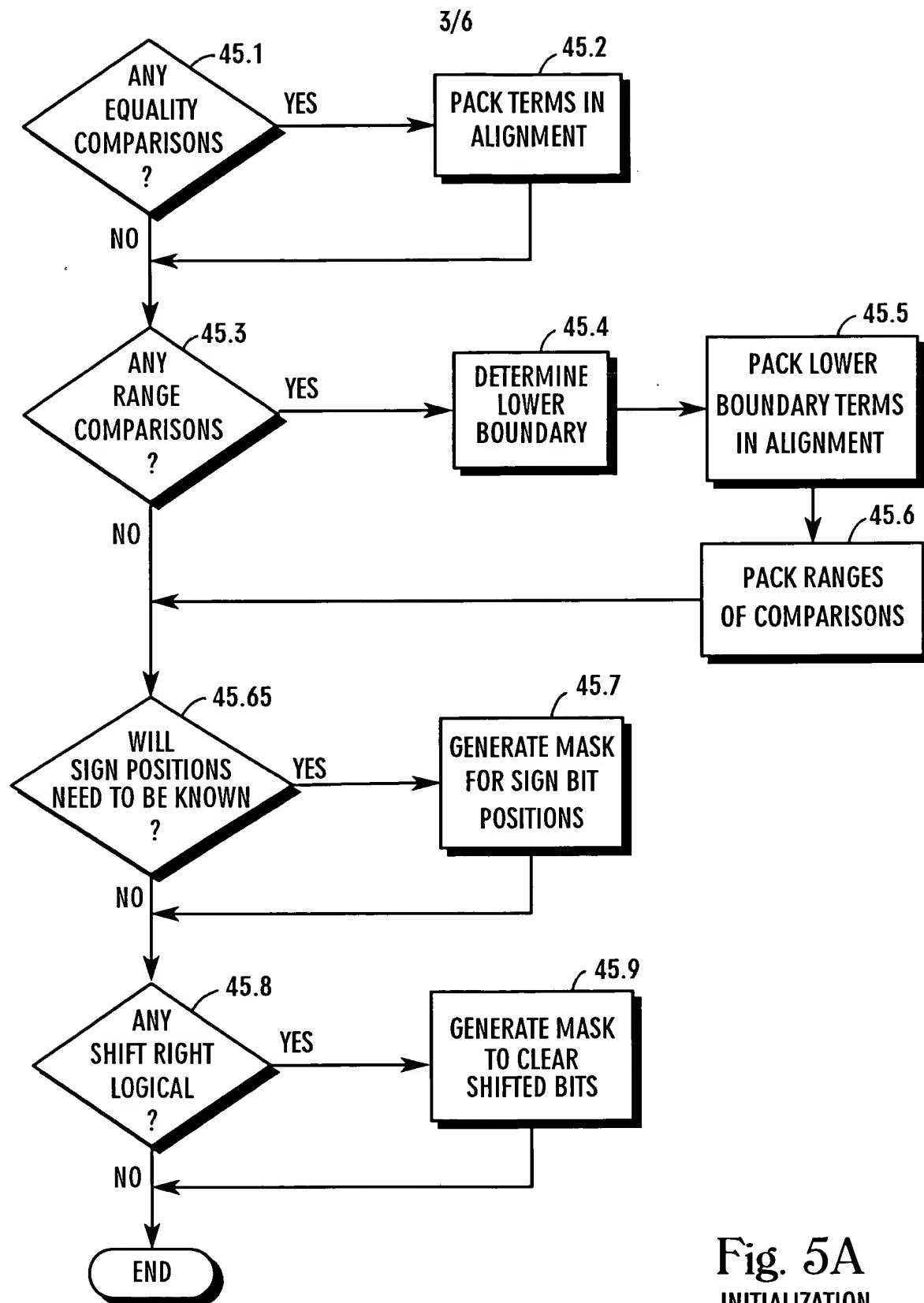
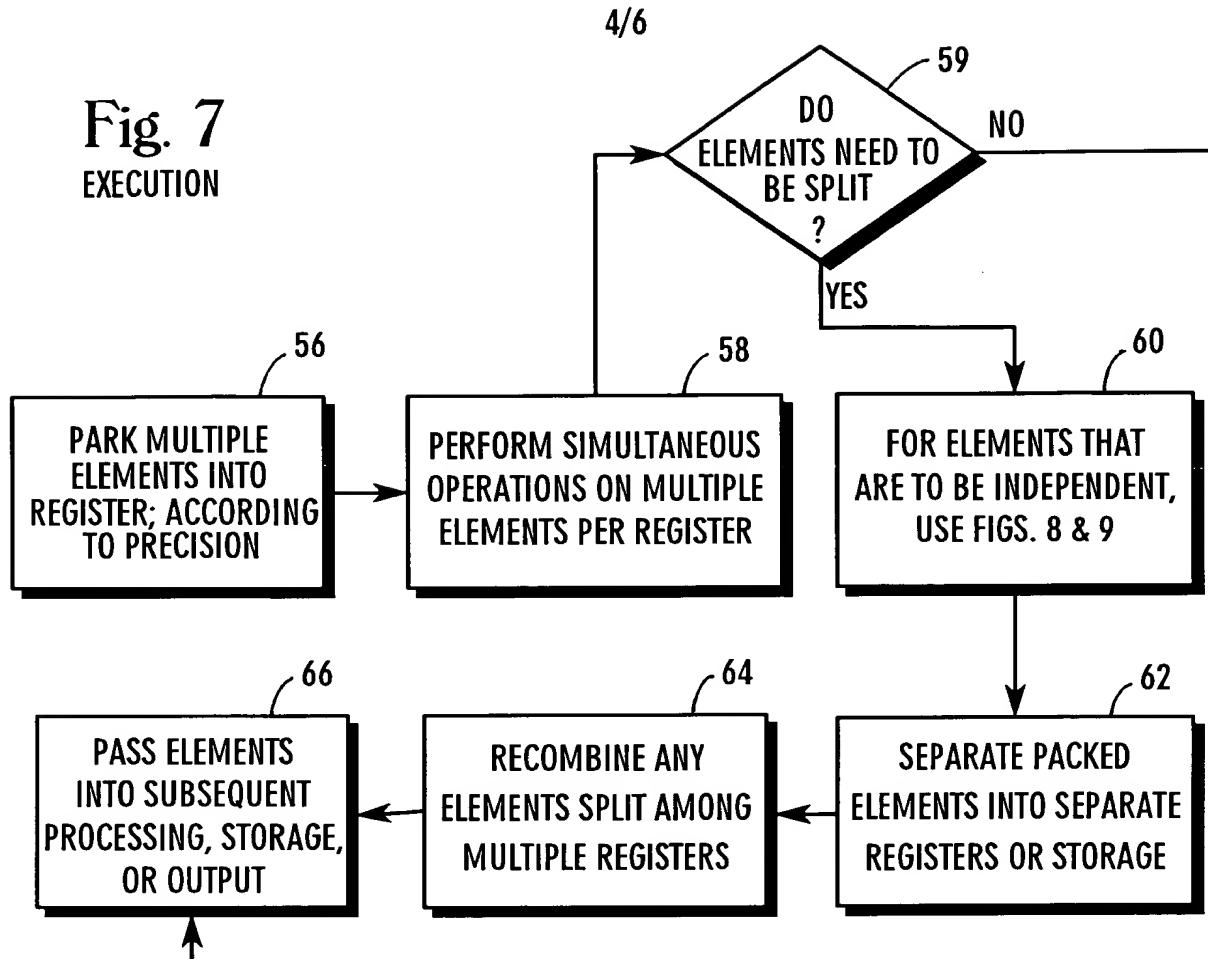
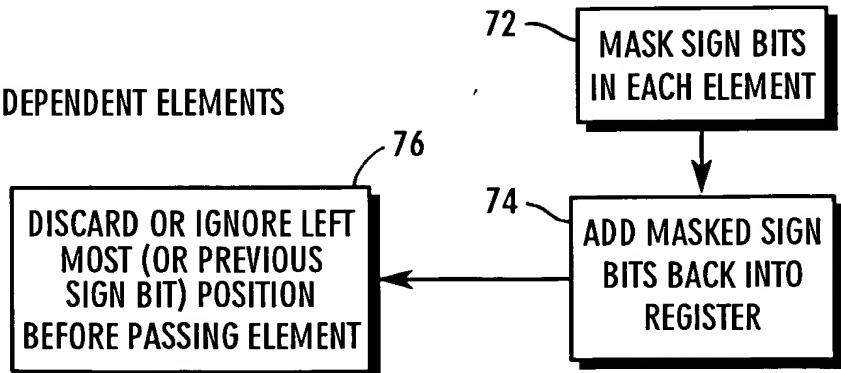
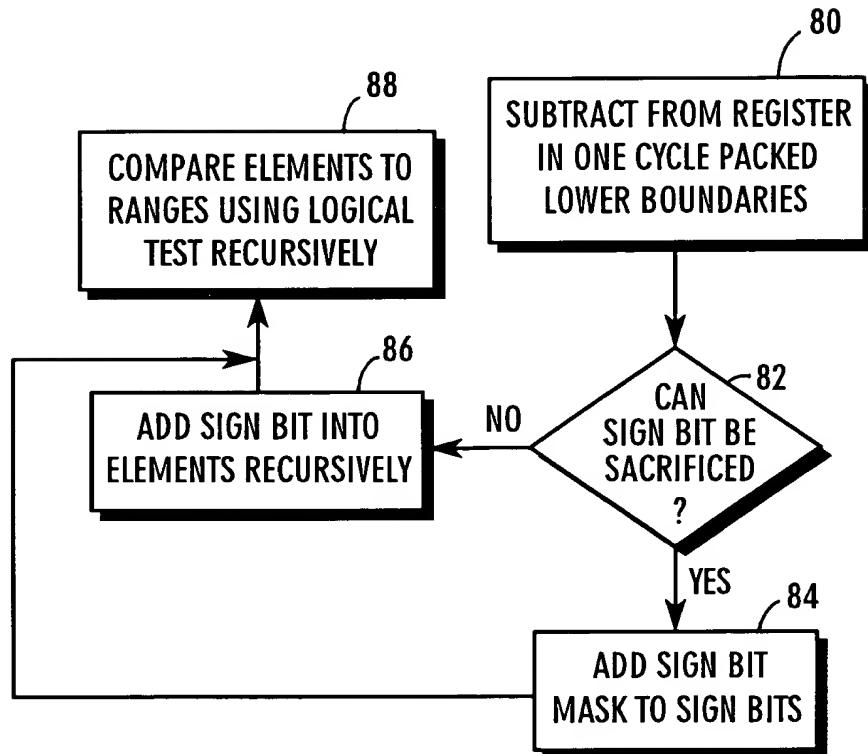


Fig. 5A
INITIALIZATION

Fig. 7
EXECUTIONFig. 8
LOGIC FOR INDEPENDENT ELEMENTSFig. 9
ALTO LOGIC FOR INDEPENDENT ELEMENTS

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Fig. 10
LOGIC FOR DETERMINING WHETHER ELEMENT
CONTAINED AROUND ZERO



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Fig. 11

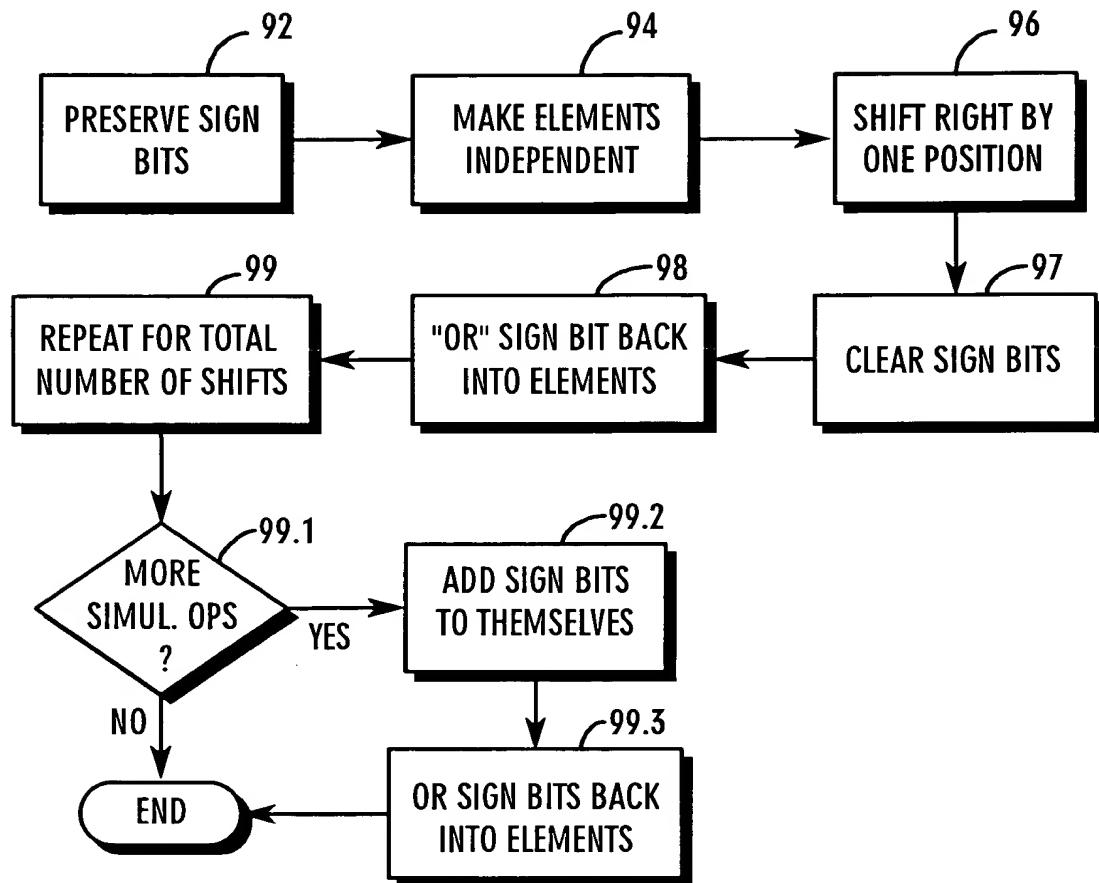


Fig. 12 LOGIC FOR ARITHMETIC COMPARES

